



Programmable Logic Handbook: PLDs, CPLDs and FPGAs

Ashok K. Sharma

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This practice-oriented guide to programming with Field Programmable Logic Devices is the most complete resource on the subject. FPLDs are an essential part of today's high-performance electronic systems because they save board space, use less power, and offer quicker turnaround times than traditional integrated circuits. However, to maximize FPLDs, designers must understand and get around the tradeoffs involved. This one-stop guide addresses the challenges and opportunities through detailed coverage of: FPGAs, PLDs, PLAs, and CPLDs; the high-level description languages VHDL and Verilog; test issues; and more.

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